



42390P6940D

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

David Fraser et al.

Examiner: Everhart, Caridad

Serial No.: 09/821,323

Art Unit: 2825

Filed: March 28, 2001

For: METHOD OF CREATING SHIELDED  
STRUCTURES TO PROTECT  
SEMICONDUCTOR DEVICES

Mail Stop Issue Fee

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**AMENDMENT UNDER 37 C.F.R. § 1.312**

Dear Sir:

In response to the Notice of Allowance mailed on July 22, 2003, please amend this claim.

**IN THE CLAIMS:**

Please amend claim 20.

20. (currently amended) The apparatus on a substrate of claim 19, wherein the the at least one vertical metal wall is made from one or more metal layers.